

REMARKS**Claim Rejections 35 U.S.C. § 102 (b)**

The Examiner has rejected claims 9, 10, and 16-28 under 35 U.S.C. § 102 (b) as being anticipated by Uzoh et al. (US 5,807,165).

Applicant respectfully disagrees with the Examiner. Applicant has amended claim 9. Support is provided in paragraphs [0013] and [0017], both on page 6 of the specification.

Claim 9, as amended, of Applicant's claimed invention claims an apparatus (100) including: a platen (110); a polishing pad (120) located over the platen; a segmented cathode (130) located between the platen and a rear surface of the polishing pad; a slurry (150) located on the polishing pad; a wafer (160) located on the polishing pad and the slurry; a wafer carrier (180) to hold the wafer; a segmented anode (170) located between a rear surface of the wafer (160) and the wafer carrier (180); a power supply (190) to apply a voltage between the segmented cathode and the segmented anode; and a computer (200), the computer to vary the voltage. See Figure 1.

In contrast, the Uzoh et al. reference cited by the Examiner teaches an apparatus (60) including a rotatable workpiece carrier (66), a plurality of workpiece electrodes (67) disposed in a recess R of the carrier, a rotatable platen (62) attached to a rotatable shaft (68), a platen electrode (63) attached to the platen, a polishing pad (64) mounted on the platen, a means for urging the carrier against the pad, a slurry supply system in fluid communication with the pad, and a source (80) to vary magnitude and polarity of current as a function of time. See Figures 8-9 and Figures 17A-17B. Also, see Col. 5, lines 44-51 and lines 10-22.

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However, Uzoh et al. does not teach an apparatus including a segmented cathode located between a platen and a rear surface of the polishing pad; a segmented anode located between a (rear) major surface (10) of the wafer and a wafer carrier; and a power supply to vary a voltage between the segmented cathode and the segmented anode.

On the contrary, Uzoh et al. teaches against the present invention by teaching that an electrode portion (67E) contacts, and electrically connects to, a side (minor) surface (S) of a conductor layer (18) or workpiece (W). See Figure 1A.

Thus, Uzoh et al. does not teach each and every element of Applicant's invention, as claimed in claim 9. Consequently, Uzoh et al. does not anticipate claim 9, as amended, of Applicant's claimed invention.

Claims 10 and 16-28 are dependent on claim 9, as amended, and, thus, are also not anticipated by Uzoh et al.

In view of the foregoing, Applicant respectfully requests the Examiner to withdraw the rejections to claims 9, 10, and 16-28 under 35 U.S.C. § 102 (b).

Applicant believes that all claims pending, including amended claim 9, are now in condition for allowance so such action is earnestly solicited at the earliest possible date.

Pursuant to 37 C.F.R. § 1.136 (a) (3), Applicant hereby requests and authorizes the U.S. Patent and Trademark Office to treat any concurrent or future reply that requires a petition for extension of time as incorporating a petition for extension of time for the appropriate length of time.

Should any additional charge or fee, including extension of time fees and fees under 37 C.F.R. § 1.16 and § 1.17, be required, or otherwise needed, please charge Deposit Account No. 50-0221.

If a telephone interview would in any way expedite the prosecution of this application, the Examiner is invited to contact the undersigned at (408) 653-7897.

Respectfully submitted,

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